

Amendment to the Claims:

The claims under examination in this application, including their current status and changes made in this paper, are respectfully presented.

1 (currently amended). A method for mapping a plurality of logical blocks to a physical block, each logical block including a plurality of logical pages arranged in groups, the plurality of logical blocks and the physical block being associated with a non-volatile memory system, the method comprising:

identifying a first logical block of the plurality of logical blocks which that meets at least one criterion, the at least one criterion comprising contents of the first logical block being associated with a number of groups of logical pages fewer than a first limit;

identifying a second logical block of the plurality of logical blocks which that is substantially complementary to the first logical block; and

providing contents associated with the identified first logical block and contents associated with the identified second logical block to the physical block.

2 (currently amended). The method of claim 1 wherein identifying the first logical block of the plurality of logical blocks which meets the at least one criterion includes determining if the physical block has further comprises a relatively low erase count.

Claim 3 is canceled.

4 (currently amended). The method of claim 1 wherein identifying the second logical block of the plurality of logical blocks which that is substantially complementary to the first logical block includes determining when the that contents associated with the second logical block are associated with less than a second predetermined number of groups of logical pages fewer than a second limit associated with the second logical block, each group associated with the second logical block including at least one logical page.

5 (currently amended). The method of claim 1 wherein identifying the second logical block of the plurality of logical blocks ~~which that~~ is substantially complementary to the first logical block includes determining ~~when that~~ a number of groups associated with the second logical block ~~which that~~ have associated data and a number of groups associated with the first logical block ~~which that~~ have associated data is less than or equal to a total number of groups of physical pages associated with the physical block.

6 (original). The method of claim 1 wherein the first logical block is mapped to the physical block when it is identified, the method further including:

reclaiming a first block associated with the second logical block after providing the contents associated with the first logical block and the contents associated with the second logical block to the physical block.

7 (original). The method of claim 1 further including:

reclaiming a first block associated with the first logical block after providing the contents associated with the first logical block and the contents associated with the second logical block to the physical block; and

reclaiming a second block associated with the second logical block after providing the contents associated with the first logical block and the contents associated with the second logical block to the physical block.

8 (currently amended). A non-volatile memory system comprising:

a non-volatile memory, the non-volatile memory having an associated physical block;

means for identifying a first logical block of a plurality of logical blocks ~~which that meets at least one criterion, each logical block including a plurality of logical pages arranged in groups, and the at least one criterion being met by the contents of the first logical block being associated with a number of groups of logical pages fewer than a first limit;~~

means for identifying a second logical block of the plurality of logical blocks ~~which that~~ is substantially complementary to the first logical block; and

means for providing contents associated with the first logical block and contents associated with the second logical block to the physical block.

9 (currently amended). The non-volatile memory system of claim 8 wherein the means for identifying the first logical block of the plurality of logical blocks which meets the at least one criterion include at least one of means for determining if the physical block has a relatively low erase count and means for determining ~~when whether the contents associated with of the first logical block are associated with less than a first predetermined a number of groups of logical pages fewer than the first limit associated with the first logical block, each group associated with the first logical block including at least one logical page.~~

10 (currently amended). The non-volatile memory system of claim 8 wherein the means for identifying the second logical block ~~which that~~ is substantially complementary to the first logical block include means for determining ~~when whether~~ the contents associated with the second logical block are associated with ~~less a number of groups of logical pages fewer than a second limit predetermined number of groups associated with the second logical block, each group associated with the second logical block including at least one logical page.~~

11 (currently amended). The non-volatile memory system of claim 8 wherein the means for identifying the second logical block ~~which that~~ is substantially complementary to the first logical block include means for determining ~~when whether~~ a number of groups of logical pages associated with the second logical block ~~which that~~ have associated data and a number of groups of logical pages associated with the first logical block ~~which that~~ have associated data is less than or equal to a total number of groups of physical pages associated with the physical block.

12 (original). The non-volatile memory system of claim 8 wherein the non-volatile memory system is one of an embedded system, a Smart Media card, a Compact Flash card, a Secure Digital Card, and a MultiMedia card.

13 (currently amended). A memory system comprising:
a non-volatile memory, the non-volatile memory having an associated physical block;

code devices that identify a first logical block of a plurality of logical blocks which that meets at least one criterion, each logical block including a plurality of logical pages arranged in groups, and the at least one criterion being met by the contents of the first logical block being associated with a number of groups of logical pages fewer than a first limit;

code devices that identify a second logical block of the plurality of logical blocks which that is substantially complementary to the first logical block;

code devices that provide contents associated with the first logical block and contents associated with the second logical block to the physical block; and

a memory area that stores the code devices.

14 (currently amended). The memory system of claim 13 wherein the code devices that identify the first logical block of the plurality of logical blocks which that meets the at least one criterion include at least one of code devices that determine if the physical block has a relatively low erase count and code devices that determine when whether the contents associated with of the first logical block are associated with less than a first predetermined a number of groups of logical pages fewer than the first limit associated with the first logical block, each group associated with the first logical block including at least one logical page.

15 (currently amended). The memory system of claim 13 wherein the code devices that identify the second logical block which that is substantially complementary to the first logical block include code devices that determine when whether the contents associated with of the second logical block are associated with less a number of groups of logical pages fewer than a second limit predetermined number of groups associated with the second logical block, each group associated with the second logical block including at least one logical page.

16 (currently amended). The memory system of claim 13 wherein the code devices that identify the second logical block which that is substantially complementary to the first logical block include code devices that determine when whether a number of groups of logical pages associated with the second logical block which that have associated data and a number of groups of logical pages associated with the first logical block which that have associated data is less than or equal to a total number of groups of physical pages associated with the physical block.

17 (original). The memory system of claim 13 wherein the first logical block is mapped to the physical block when it is identified, and the memory system further includes:

code devices that reclaim a first block associated with the second logical block after the contents associated with the first logical block and the contents associated with the second logical block are provided to the physical block.

18 (original). The memory system of claim 13 further including:

code devices that reclaim a first block associated with the first logical block after the contents associated with the first logical block and the contents associated with the second logical block are provided to the physical block; and

code devices that reclaim a second block associated with the second logical block after the contents associated with the first logical block and the contents associated with the second logical block are provided to the physical block.

19 (original). The memory system of claim 13 wherein the non-volatile memory system is one of n embedded system, a Smart Media card, a Compact Flash card, a Secure Digital Card, and a MultiMedia card.

Claims 20 through 42 are canceled.